

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	2609	(evaluat\$4 and pre\$2charg\$4) and (delay\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	710	1 and (tri\$2state (high adj impedance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L4	498	2 and ((clock (precharg\$3)) same delay\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L5	619	2 and (half\$3latch\$3 full\$3latch\$3 latch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB